

Docket No.: 060188-0891



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
Shiro DOSHO, et al.	:	Confirmation Number: 1177
Application No.: 10/500,875	:	
Patent No.: 7,030,688	:	Group Art Unit: 2816
Filed: July 07, 2004	:	
Issued: April 18, 2006	:	Examiner: Dinh Thanh Le

For: LOW-PASS FILTER FOR A PLL, PHASE-LOCKED LOOP AND SEMICONDUCTOR INTEGRATED CIRCUIT

**REQUEST FOR CERTIFICATE OF CORRECTION UNDER 37 CFR 1.322**

Mail Stop COC  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**Certificate**  
**SEP 14 2006**  
**of Correction**

In reviewing the above-identified patent, a printing error was discovered therein requiring correction in order to conform the Official Record in the application.

The error noted is set forth on the two attached copies of form PTO-1050 Rev. 2-93 in the manner required by the Commissioner's Notice.

Specifically, On the Title page of the Letters Patent, under section "(75) Inventors", change:  
" Ikeda " to -- Osaka --, " Takatsuki " to -- Osaka --, " Uji " to -- Kyoto --, " Ibaraki " to -- Osaka --,  
" Katano " to -- Osaka --, " Yawata " to -- Kyoto --, and " Takatsuki " to -- Osaka --. For your  
immediate reference a photocopy of the Declaration and Power of Attorney is attached.

**SEP 15 2006**

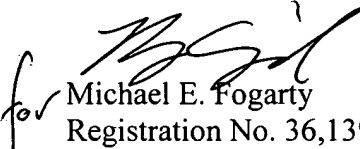
Patent No.: 7,030,688

The change requested herein occurred as a result of printing the Letters Patent and the Certificate should be issued without expense under Rule 322 of the Rules of Practice. Accordingly, Applicants request issuance of the Certificate of Correction.

Please charge any shortage in fees due in connection with the filing of this paper to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

*for*  #46,692  
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**Date: September 12, 2006**

**Please recognize our Customer No. 20277  
as our correspondence address.**

SEP 15 2006

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7030688  
DATED : April 18, 2006  
INVENTOR(S) : Shiro DOSHO, et al.

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title page of the Letters Patent,

Under section "(75) Inventors", change:

" Ikeda " to -- Osaka --,  
" Takatsuki " to -- Osaka --,  
" Uji " to -- Kyoto --,  
" Ibaraki " to -- Osaka --,  
" Katano " to -- Osaka --,  
" Yawata " to -- Kyoto --,  
" Takatsuki " to -- Osaka --

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USA

PATENT NO.  
7,030,688

No. of add'l copies  
@ 50¢ per page

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7,030,688

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}



Docket No. \_\_\_\_\_

**COMBINED DECLARATION/POWER OF ATTORNEY**  
**FOR PATENT APPLICATION**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled **LOW-PASS FILTER, FEEDBACK SYSTEM AND SEMICONDUCTOR INTEGRATED CIRCUIT**, the specification of which

(check one) \_\_\_\_\_ is attached hereto.

\_\_\_\_\_ was filed on \_\_\_\_\_ as  
Application Serial No. \_\_\_\_\_.

X PCT International Patent Application Number **PCT/JP03/06428**  
filed **May 22, 2003**  
and was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Claimed
<u>2002-148129</u> (Number)	<u>JAPAN</u> (Country)	<u>22/05/2002</u> (Day/Month/Year Filed)	<u>X</u> Yes ___ No
<u>2002-262793</u> (Number)	<u>JAPAN</u> (Country)	<u>09/09/2002</u> (Day/Month/Year Filed)	<u>X</u> Yes ___ No
<u>2002-286987</u> (Number)	<u>JAPAN</u> (Country)	<u>30/09/2002</u> (Day/Month/Year Filed)	<u>X</u> Yes ___ No

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Appln. Serial No.)	(Filing Date)	(Status-patented, pending, abandoned)
(Appln. Serial No.)	(Filing Date)	(Status-patented, pending, abandoned)

I hereby appoint as my attorneys, with full power of substitution and revocation, to prosecute the patent application identified above and to transact all business in the U.S. Patent and Trademark Office connected therewith: Raphael V. Lupo (Reg. No. 28,363); Jack Q. Lever, Jr. (Reg. No. 28,149); Kenneth L. Cage (Reg. No. 26,151); Stanislaus Aksman (Reg. No. 28,562); Michael E. Fogarty (Reg. No. 36,139); Brian E. Ferguson (Reg. No. 36,801); Robert W. Zelnick (Reg. No. 36,976); Edward E. Kubasiewicz (Reg. No. 30,020); Paul Devinsky (Reg. No. 28,553); and Wilhlem F. Gadiano (Reg. No. 37,136); Laura A. Donnelly (Reg. No. 38,435); Craig L. Plastrik (Reg. No. 41,254); David A. Spenard (Reg. No. 37,449)

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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